

VC-155M-SFP-ZX

155Mbps DFB SFP Optical Transceiver, 80km Reach

Features

- ✓ Data-rate of 155Mbps operation
- ✓ DFB 1550nm wavelengths laser and PIN photodetector for 80km transmission
- ✓ Compliant with SFP MSA and SFF-8472 with duplex LC receptacle
- ✓ Digital Diagnostic Monitoring:
- ✓ Internal Calibration or External Calibration
- ✓ Compatible with RoHS
- ✓ +3.3V single power supply
- ✓ Operating case temperature:
- ✓ Standard : 0 to +70°C
Industrial : -40 to +85°C

Applications

- ✓ Gigabit Ethernet
- ✓ Fiber Channel
- ✓ Switch to Switch interface
- ✓ Switched backplane applications
- ✓ Router/Server interface
- ✓ Other optical transmission systems

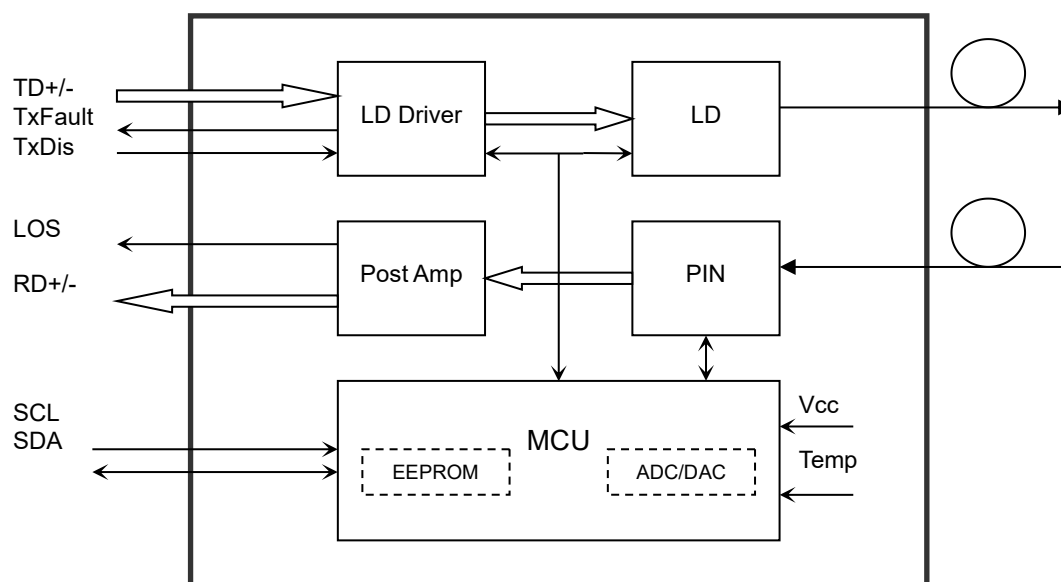
Description

The SFP transceivers are high performance, cost effective modules supporting data-rate of 155Mbps and 80km transmission distance with SMF.

The transceiver consists of three sections: a uncooled DFB laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement (MSA) and SFF-8472. For further

information, please refer to SFP MSA.



Absolute Maximum Ratings

Table 1 - Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	95	%

Recommended Operating Environment

Table 2 - Recommended Operating Environment

Parameter		Symbol	Min	Typical	Max	Unit
Operating Temperature	Case Standard	Tc	0		+70	°C

Power Supply Voltage	V _{cc}	3.13	3.3	3.47	V
Power Supply Current	I _{cc}			300	mA
Data Rate			155		Mbps

Optical and Electrical Characteristics

VC-155M-SFP-ZX: (DFB and PIN, 80km Reach)

Table 3 - Optical and Electrical Characteristics

Parameter		Symbol	Min	Typical	Max	Unit	Notes
Transmitter							
Centre Wavelength		λ_c	1520	1550	1580	nm	
Spectral Width (-20dB)		$\Delta\lambda$			1	nm	
Side Mode Suppression Ratio		SMSR	30			dB	
Average Output Power		P _{out}	0		5	dBm	1
Extinction Ratio		ER	10			dB	
Jitter Generation (RMS)					0.01	UI	
Jitter Generation (PK-PK)					0.1	UI	
Output Optical Eye		Compliant Telcordia GR-253-CORE and ITU-T G.957					
Optical Rise/Fall Time (20%~80%)		t _r /t _f			0.26	ns	
Data Input Swing Differential		V _{IN}	300		1860	mV	2
Input Differential Impedance		Z _{IN}	90	100	110	Ω	
TX Disable	Disable		2.0		V _{cc}	V	
	Enable		0		0.8	V	
TX Fault	Fault		2.0		V _{cc}	V	
	Normal		0		0.8	V	
Receiver							
Centre Wavelength		λ_c	1260		1580	nm	
Receiver Sensitivity					-34	dBm	3
Receiver Overload			-9			dBm	3
LOS De-Assert		LOS _D			-38	dBm	
LOS Assert		LOS _A	-45			dBm	
LOS Hysteresis			1		4	dB	
Data Output Swing		V _{out}	370		1800	mV	4

Differential						
LOS	High	2.0		V _{cc}	V	
	Low	0		0.8	V	

Notes:

1. The optical power is launched into SMF.
2. PECL input, internally AC-coupled and terminated.
3. Measured with a PRBS 2²³-1 test pattern @155Mbps, BER ≤1×10⁻¹².
4. Internally AC-coupled.

Timing and Electrical
Table 4 - Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t _{on}			1	ms
Tx Disable Assert Time	t _{off}			10	μs
Time To Initialize, including Reset of Tx Fault	t _{init}			300	ms
Tx Fault Assert Time	t _{fault}			100	μs
Tx Disable To Reset	t _{reset}	10			μs
LOS Assert Time	t _{loss_on}			100	μs
LOS De-assert Time	t _{loss_off}			100	μs
Serial ID Clock Rate	f _{serial_clock}			400	KHz
MOD_DEF (0:2)-High	V _H	2		V _{cc}	V
MOD_DEF (0:2)-Low	V _L			0.8	V

EEPROM Information

The SFP MSA defines a 256-byte memory map in EEPROM describing the transceiver's capabilities, standard interfaces, manufacturer, and other information, which is accessible over a 2 wire serial interface at the 8-bit address 1010000X (A0h). The memory contents refer to Table 6.

Table 5 - EEPROM Serial ID Memory Contents (A0h)

Addr.	Field Size (Bytes)	Name of Field	Hex	Description
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0	1	Identifier	03	SFP
1	1	Ext. Identifier	04	MOD4
2	1	Connector	07	LC
3—10	8	Transceiver	00 08 04 00 00 00 00 00	OC-3,Single mode long reach
11	1	Encoding	03	NRZ
12	1	BR, nominal	02	155Mbps
13	1	Reserved	00	
14	1	Length (9um)-km	78	80km
15	1	Length (9um)	FF	80km
16	1	Length (50um)	00	
17	1	Length (62.5um)	00	
18	1	Length (copper)	00	
19	1	Reserved	00	
20—35	16	Vendor name		"ASCENTOPTICS"(ASCII)
36	1	Reserved	00	
37—39	3	Vendor OUI	00 00 00	
40—55	16	Vendor PN	47 50 2D 35 35 30 33 2D 31 32 43 44 20 20 20 20	“VC-155M-SFP-ZX ”(ASCII)
56—59	4	Vendor rev	xx xx xx xx	xx.xx revision (ASCII)
60-61	2	Wavelength	0A AA	1550nm
62	1	Reserved	00	
63	1	CC BASE	xx	Check sum of bytes 0 - 62
64—65	2	Options	00 1A	LOS,TX_FAULT and TX_DISABLE
66	1	BR, max	00	
67	1	BR, min	00	

68—83	16	Vendor SN	xx xx xx xx xx xx xx xx xx xx xx xx xx xx xx xx	ASCII
84—91	8	Vendor date code	xx xx xx xx xx xx 20 20	Year(2 bytes),Month(2 bytes),Day(2 bytes)
92	1	Diagnostic type	xx	Diagnostics
93	1	Enhanced option	80	Diagnostics(Optional Alarm/warning flags)
94	1	SFF-8472	01	Diagnostics(SFF-8472 Rev 9.3)
95	1	CC EXT	xx	Check sum of bytes 64 - 94
96—255	160			

Diagnostics

Table 6 – Diagnostics Specification

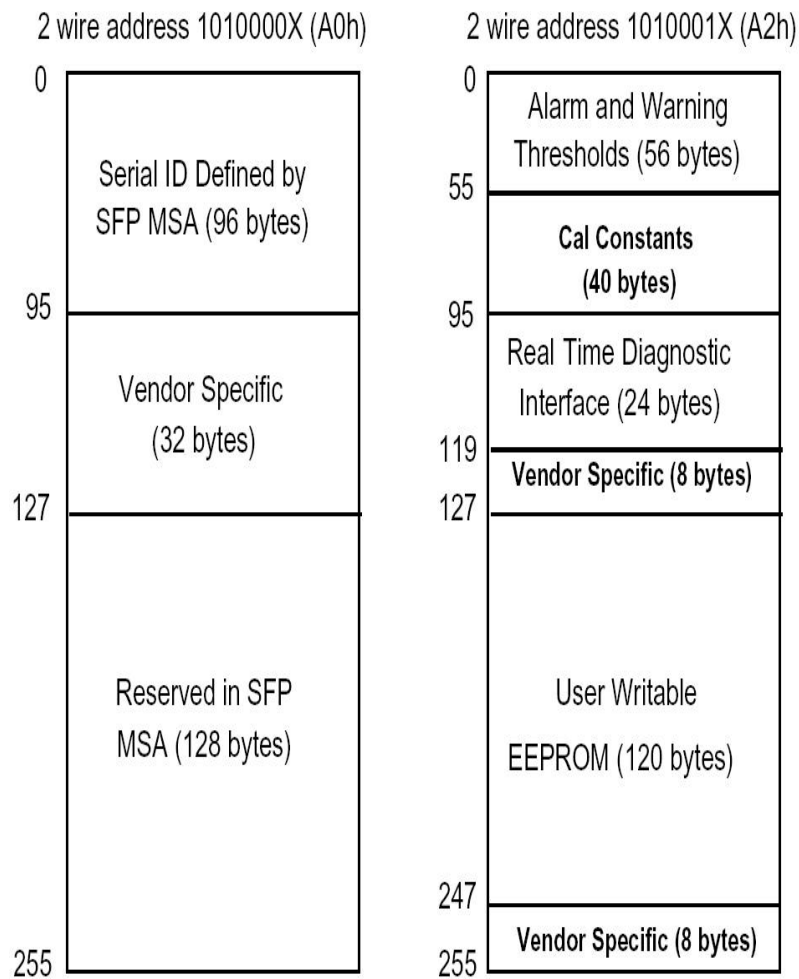
Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal / External
Voltage	3.0 to 3.6	V	±3%	Internal / External
Bias Current	0 to 100	mA	±10%	Internal / External
TX Power	0 to +5	dBm	±3dB	Internal / External
RX Power	-33 to -9	dBm	±3dB	Internal / External

Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

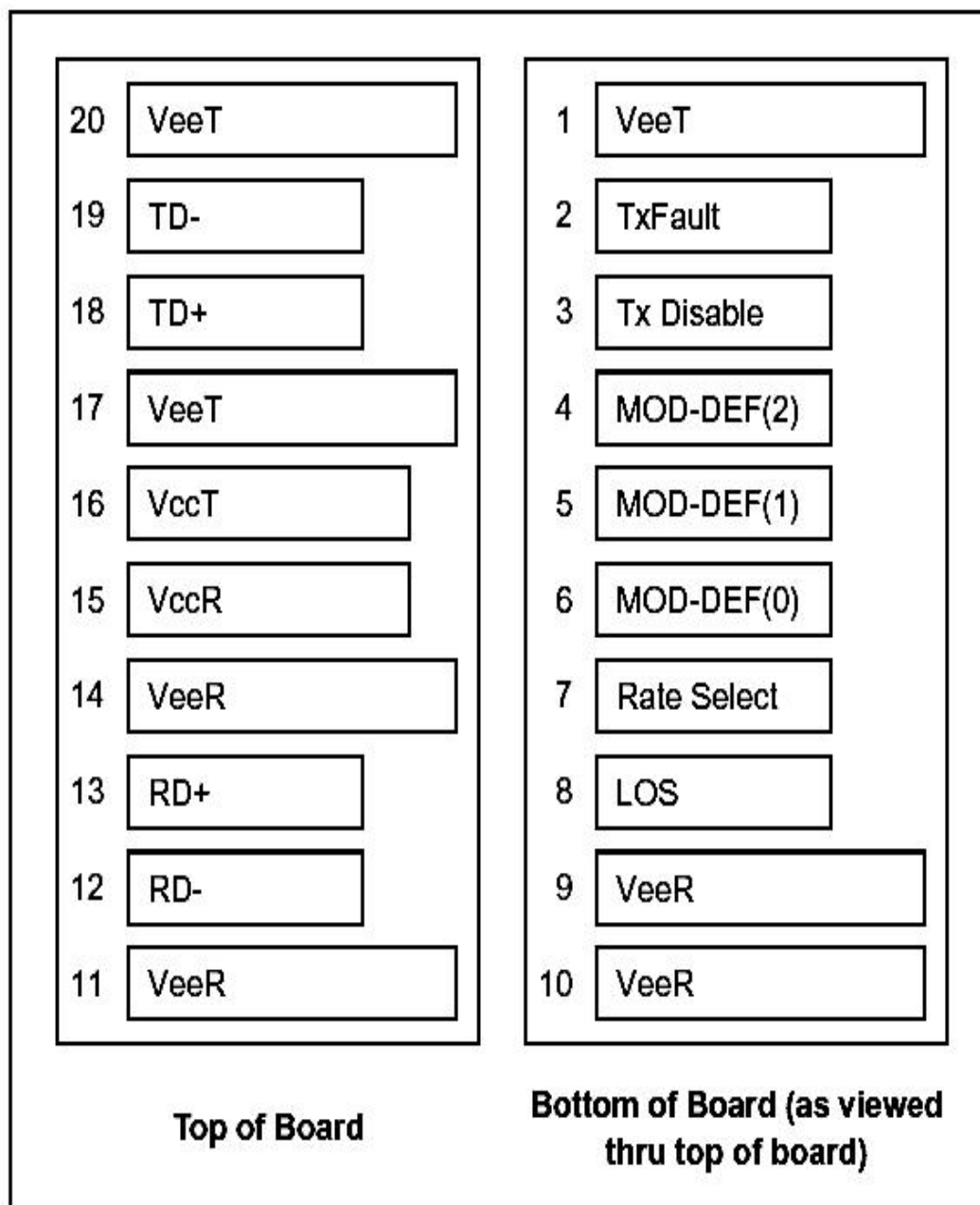
The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.



Pin Assignment

Pin Diagram



Pin Descriptions

Pin	Signal Name	Description	Plug Seq.	Notes
1	V _{EET}	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	MOD_DEF(2)	SDA Serial Data Signal	3	Note 3

5	MOD_DEF(1)	SCL Serial Clock Signal	3	Note 3
6	MOD_DEF(0)	TTL Low	3	Note 3
7	Rate Select	Not Connected	3	
8	LOS	Loss of Signal	3	Note 4
9	V _{EER}	Receiver ground	1	
10	V _{EER}	Receiver ground	1	
11	V _{EER}	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 5
13	RD+	Received Data Out	3	Note 5
14	V _{EER}	Receiver ground	1	
15	V _{CCR}	Receiver Power Supply	2	
16	V _{CCT}	Transmitter Power Supply	2	
17	V _{EET}	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 6
19	TD-	Inv. Transmit Data In	3	Note 6
20	V _{EET}	Transmitter Ground	1	

Notes:

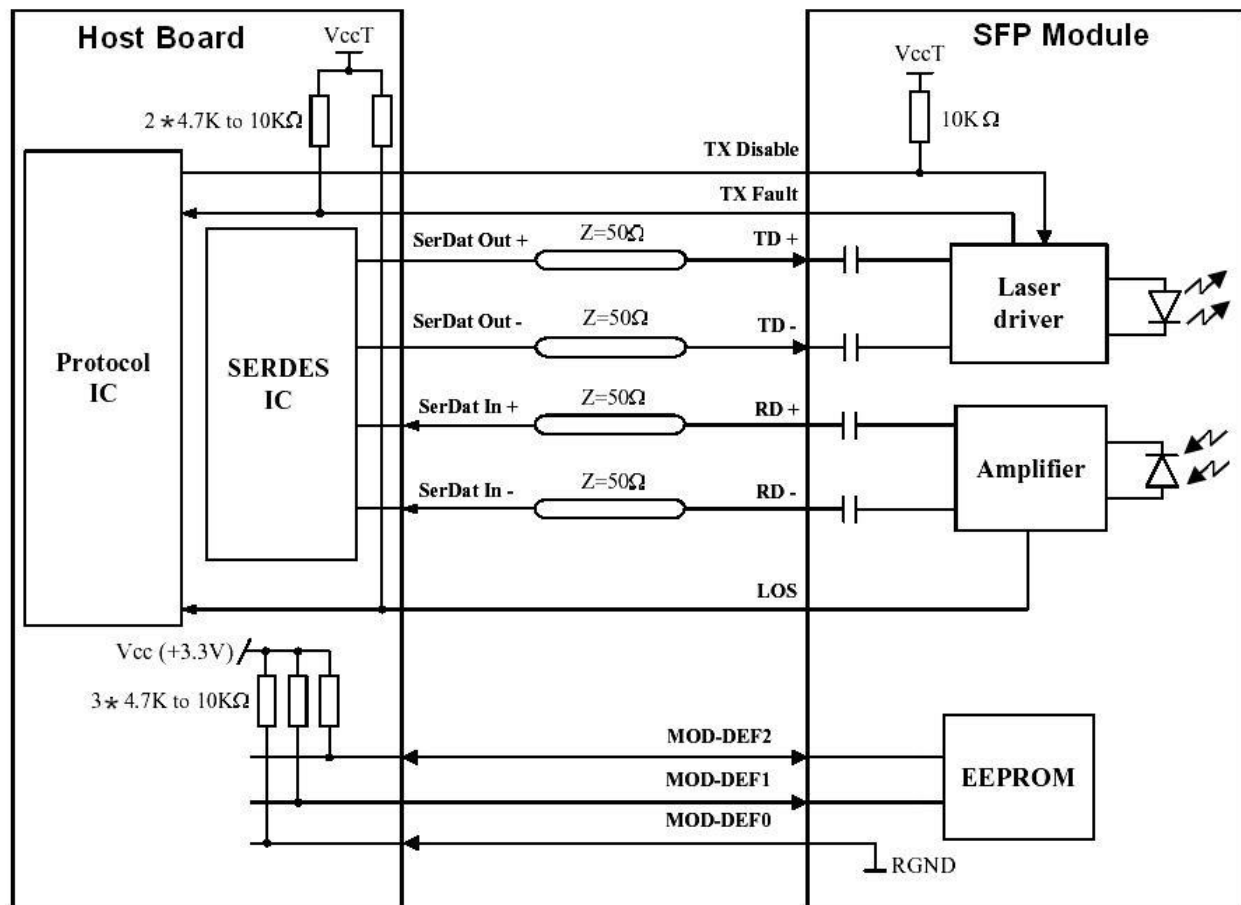
Plug Seq.: Pin engagement sequence during hot plugging.

- 1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7k~10kΩ resistor. Its states are:

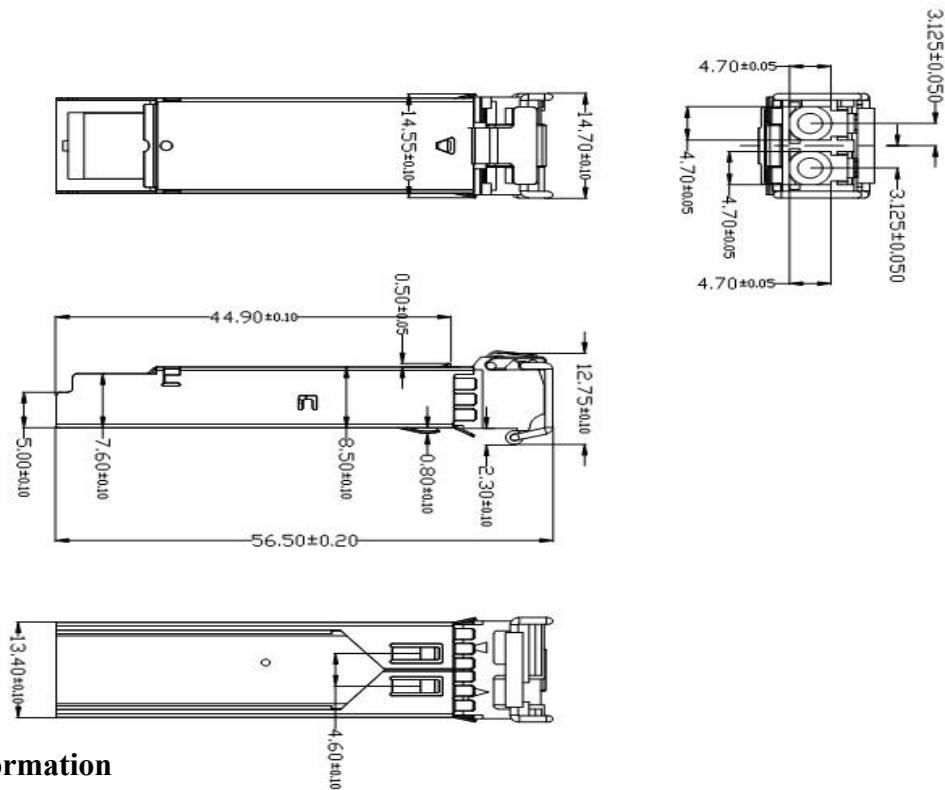
Low (0 to 0.8V):	Transmitter on
(>0.8V, < 2.0V):	Undefined
High (2.0 to 3.465V):	Transmitter Disabled
Open:	Transmitter Disabled
- 3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7k~10kΩ resistor on the host board. The pull-up voltage shall be VccT or VccR.
 Mod-Def 0 is grounded by the module to indicate that the module is present
 Mod-Def 1 is the clock line of two wire serial interface for serial ID
 Mod-Def 2 is the data line of two wire serial interface for serial ID
- 4) LOS is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor. Pull up voltage between 2.0V and Vcc+0.3V. Logic 1 indicates loss of signal; Logic 0 indicates normal operation. In the low state, the output will be pulled to less than 0.8V.
- 5) RD-/+ : These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.

- 6) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

Recommended Interface Circuit



Mechanical Dimensions



Ordering information

Part Number	Product Description
VC-155M-SFP-ZX-80C	1550nm DFB, 155Mbps, 80km, $0^{\circ}\text{C} \sim +70^{\circ}\text{C}$, With DDM
VC-155M-SFP-ZX-80T	1550nm DFB, 155Mbps, 80km, $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$, With DDM